ADAPTER METHOD AND APPARATUS FOR INTERFACING A TESTER WITH A DEVICE UNDER TEST

BACKGROUND OF THE INVENTION

Cross Reference to Related Applications:

[0001] This invention is a continuation-in-part of patent application serial number 10/103,392, filed on March 21, 2002, and entitled Adaptor method and apparatus for interfacing a tester with a device under test. PAT 6,828,773

Field of the Invention:

[0002] This invention relates to electronic systems. Specifically, the present invention relates to electronic test systems.

Description of the Related Art:

[0003] Modern electronic systems are sophisticated and complex. As a result, supporting technology such as testing technology has increased in complexity. Electronic systems that once included hundreds or thousands of circuits now include millions of circuits. As a result, more sophisticated electronic testers are being deployed to test these electronic systems.

[0004] Modern electronic systems are often deployed in printed circuit boards (PCB's) or in integrated circuits with multiple analog and discrete components. Both the printed circuit boards and the integrated circuits include millions of devices (e.g. transistors, logic gates, etc). Since printed circuit boards and integrated circuits are relatively small in dimension, these devices are deployed within a small area. As a